

Fig. 1

A METHOD OF AUTO-FINDING AND NOTIFYING INCONSISTENCY OF BUILDING BLOCKS AND
ASSOCIATED TECHNOLOGY RELATED SUBSTANCE OF CHIP LEVEL DURING LOGIC AND PHYSICAL
DESIGN PHASE

"The Virtual Fab"

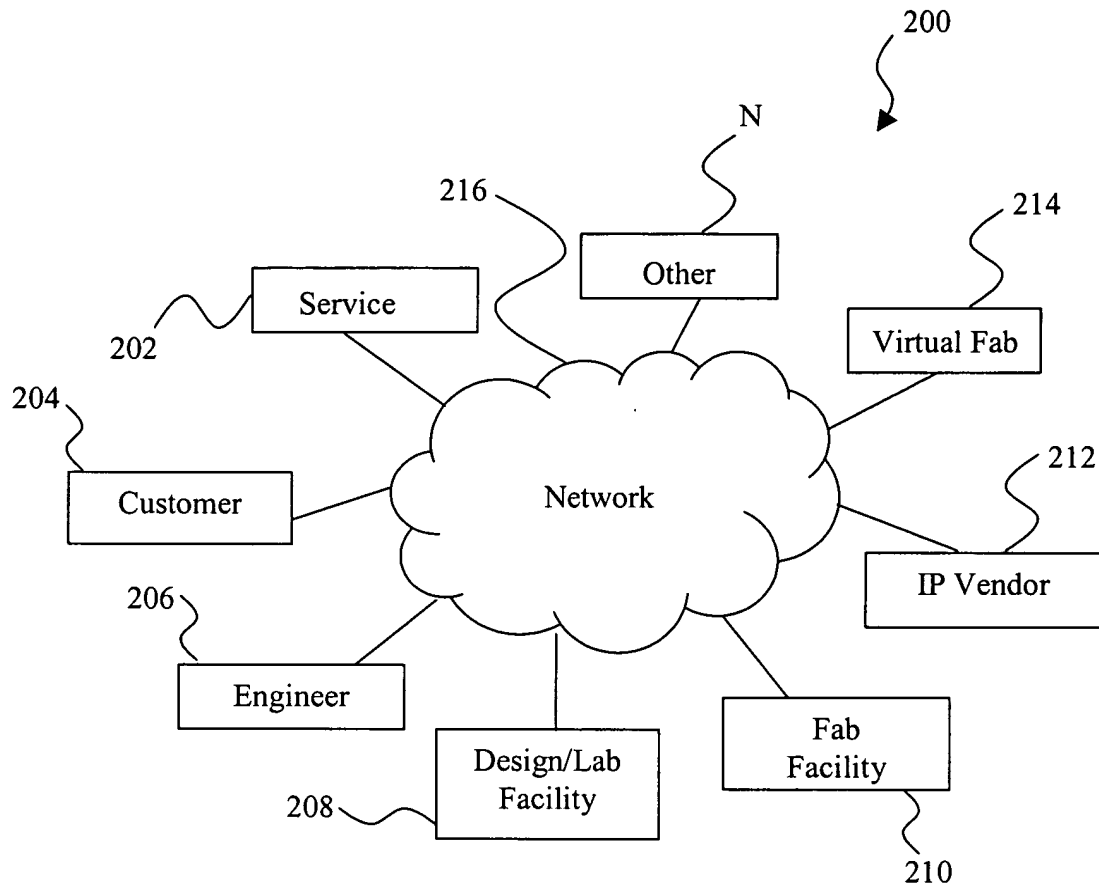


Fig. 2

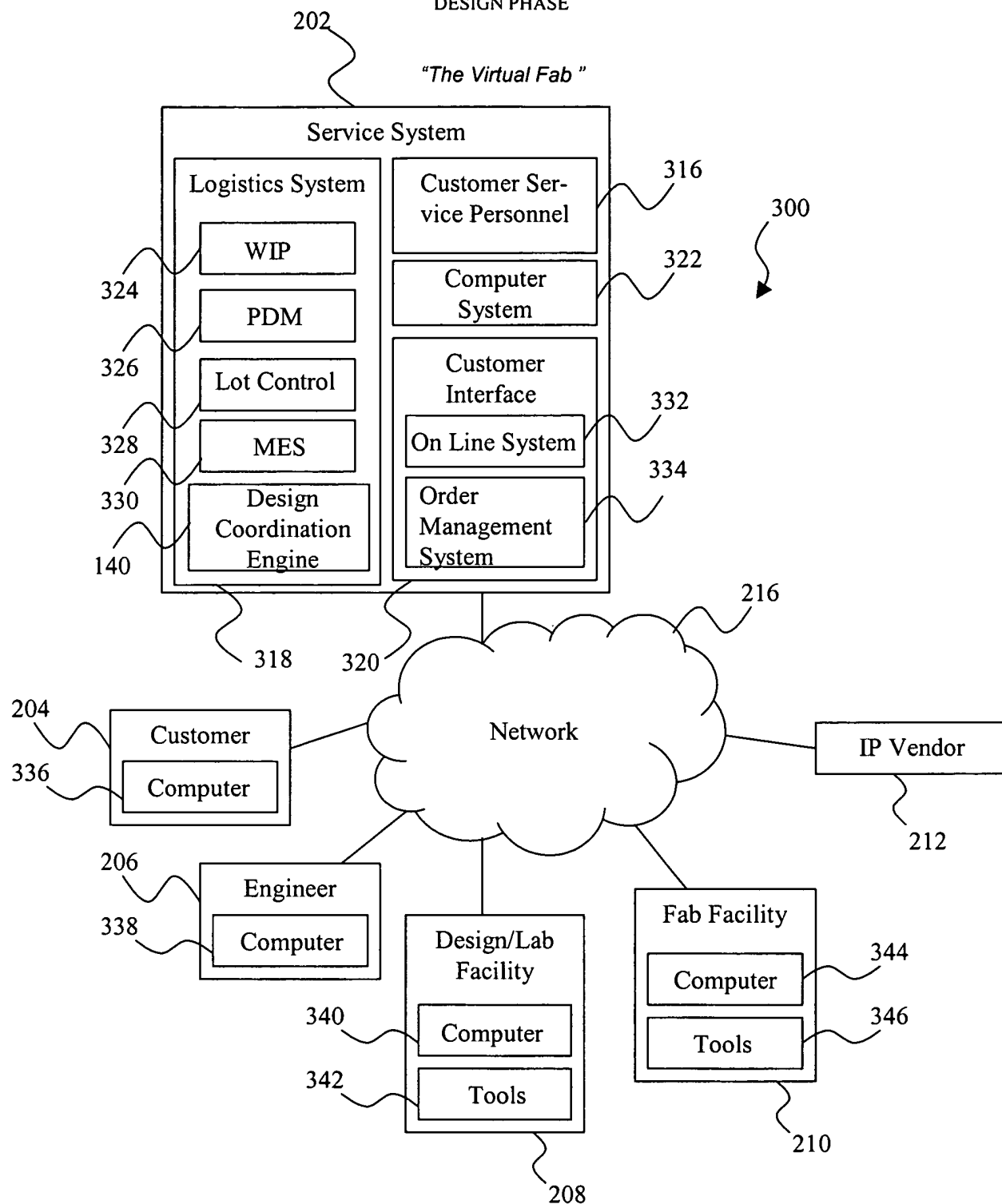


Fig. 3

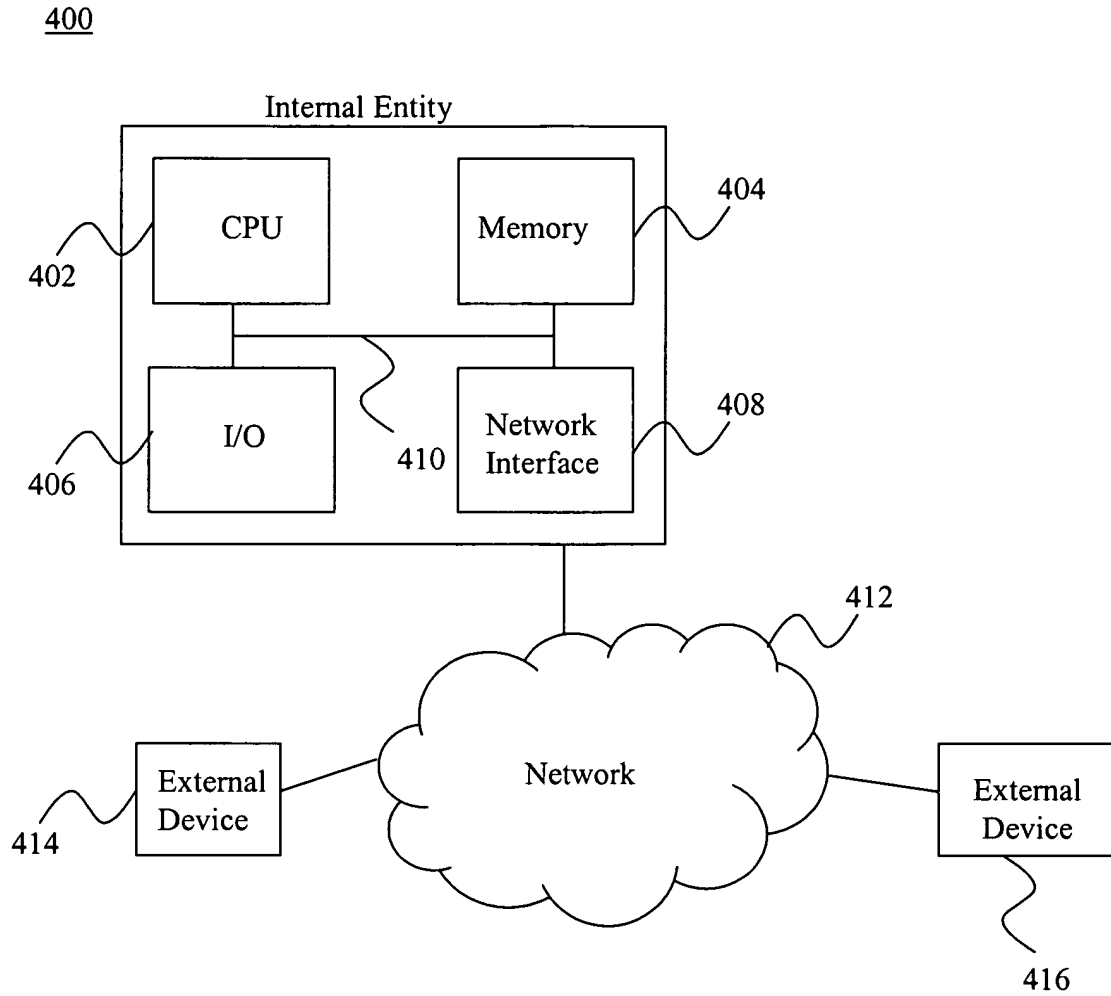


Fig. 4

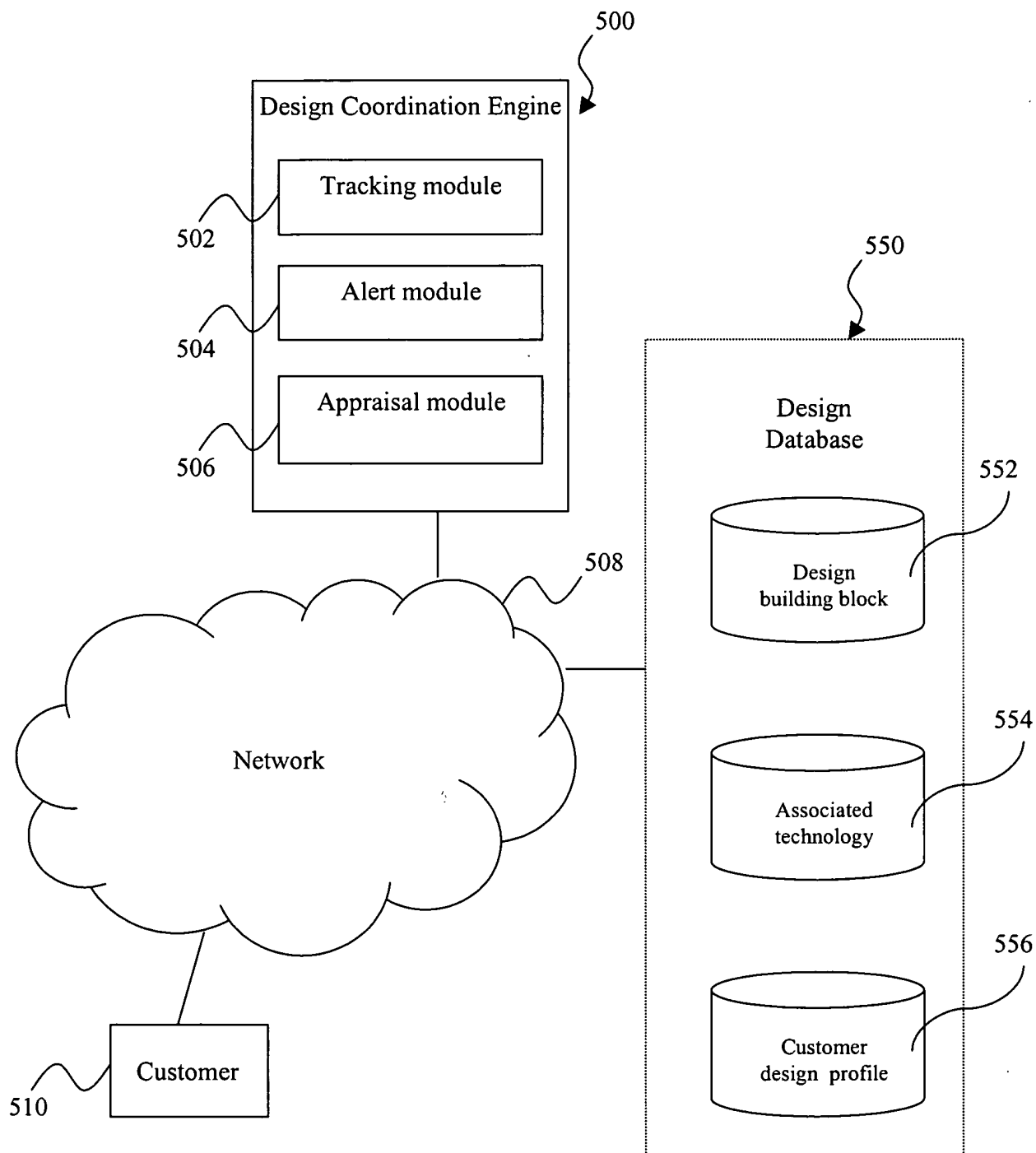


Fig. 5

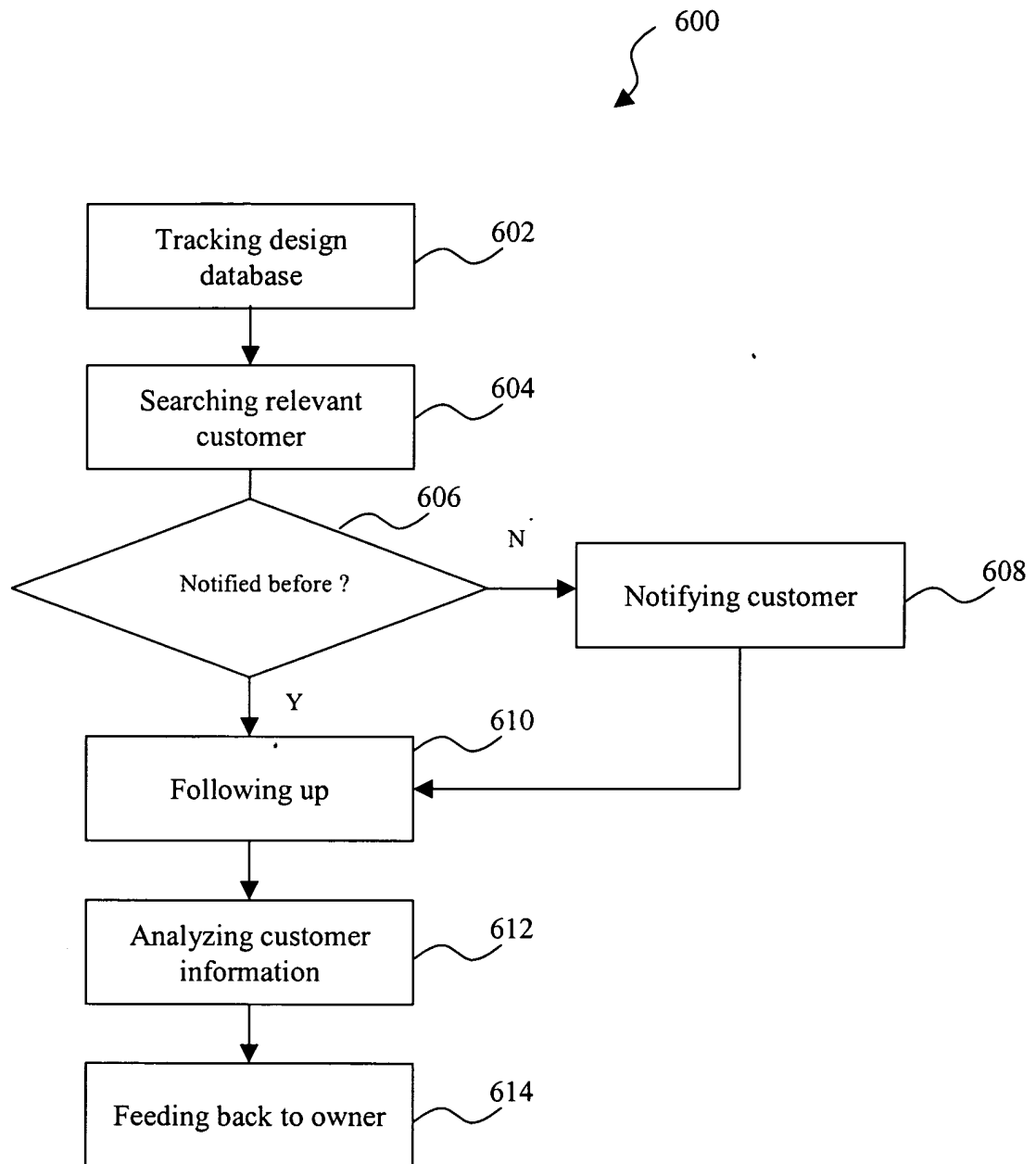


Fig. 6

Attorney Docket No. 24061.103
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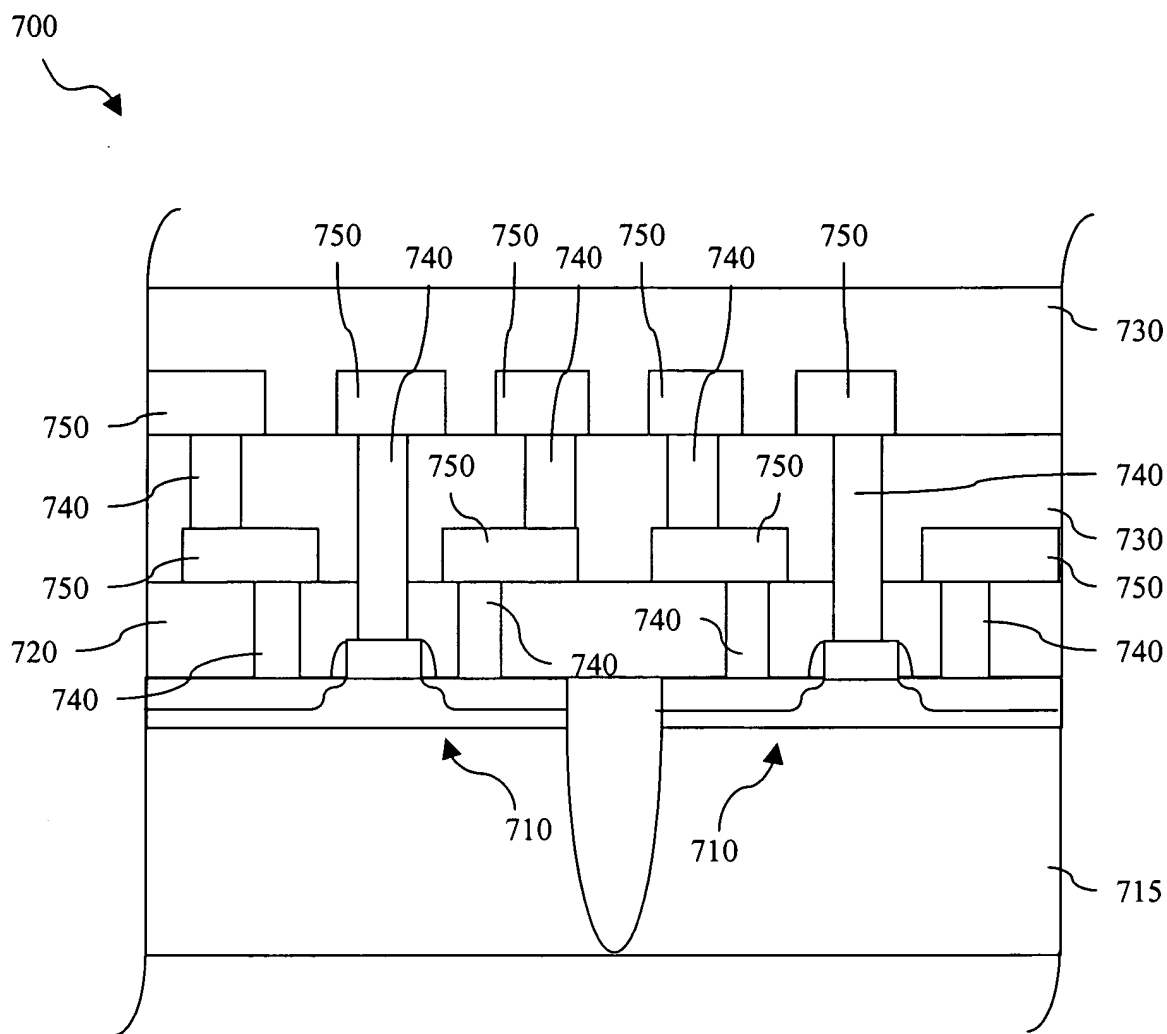


Fig. 7